



## Linearization and Efficiency Enhancement Techniques for Silicon Power Amplifiers: From RF to MmW (Hardback)

By -

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Hardback. Book Condition: New. 226 x 152 mm. Language:

English . Brand New Book. This book provides an overview of

current efficiency enhancement and linearization techniques for

silicon power amplifier designs. It examines the latest state of the

art technologies and design techniques to address challenges for

RF cellular mobile, base stations, and RF and mmW WLAN

applications. Coverage includes material on current silicon

(CMOS, SiGe) RF and mmW power amplifier designs, focusing on

advantages and disadvantages compared with traditional GaAs

implementations. With this book you will learn: \* The principles

of linearization and efficiency improvement techniques\* The

architectures allowing the optimum design of multimode Si RF

and mmW power amplifiers\* How to make designs more

efficient by employing new design techniques such as

linearization and efficiency improvement\* Layout

considerations\* Examples of schematic, layout, simulation and

measurement results \* Addresses the problems of high power

generation, faithful construction of non-constant envelope

constellations, and efficient and well control power radiation

from integrated silicon chips\* Demonstrates how silicon

technology can solve problems and trade-offs of power

amplifier design, including price, size, complexity and efficiency\*

Written and edited by the top contributors to the field.



**READ ONLINE**

### Reviews

*This book is great. It is written in simple words and not difficult to understand. I discovered this pdf from my dad and i suggested this ebook to find out.*

-- **Prof. Webster Barrows**

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-- **Heloise Dare**